

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 10/340,126
Priority Filing Date January 10, 2003
 Inventor Zhongze Wang
 Assignee Micron Technology, Inc.
Priority Group Art Unit 2812
Priority Examiner Jennifer M. Kennedy
 Attorney Docket No. MI22-2343
 Title: Method of Forming Silicon-on-Insulator Comprising Integrated
 Circuitry

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a divisional application of co-pending application Serial No. 10/340,126, filed January 10, 2003. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned. 37 C.F.R. § 1.98(d) and MPEP § 609(2).

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 6-27-03

By



Mark S. Matkin
 Reg. No. 32,268

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTORNEY DOCKET NO. MI22-2343		PRIORITY SERIAL NO. 10/340,126	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT: Zhongze Wang			
				PRIORITY FILING DATE January 10, 2003		PRIORITY GROUP ART UNIT: 2812	
U.S. PATENT DOCUMENTS							
*Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	5,021,843	06/04/91	Ohmi			
	AB	6,410,938 B1	06/25/02	Xiang			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
	AJ						
	AK						
	AL						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
	AM		<i>Floating Body Effects</i> , SOI CIRCUIT DESIGN CONCEPTS: SOI DEVICE ELECTRICAL PROPERTIES, pp. 34-53 (pre-August 2001).				
	AN						
	AO						
EXAMINER		DATE CONSIDERED					
<p><small>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small></p>							